

**PATENT** 

#### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants

Chantal Auricchio et al.

Application No.

10/760,637

Filed

January 20, 2004

For

NONVOLATILE SWITCH, IN PARTICULAR FOR HIGH-

DENSITY NONVOLATILE PROGRAMMABLE-LOGIC

**DEVICES** 

Art Unit

: 2818

Docket No.

: 854063.737

Date

June 28, 2004

Mail Stop Missing Parts Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## INFORMATION DISCLOSURE STATEMENT

### Commissioner for Patents:

In accordance with 37 C.F.R. §§ 1.56 and 1.97 through 1.98, applicants wish to make known to the Patent and Trademark Office the references set forth on the attached form PTO-1449 (copies of the cited references are enclosed). As to any reference supplied, applicants do not admit that it is "prior art" under 35 U.S.C. §§ 102 or 103, and specifically reserve the right to traverse or antedate any such reference, as by a showing under 37 C.F.R. § 1.131 or other method. Although the aforesaid references are made known to the Patent and Trademark Office in compliance with applicants' duty to disclose all information they are aware of which is believed relevant to the examination of the above-identified application, applicants believe that their invention is patentable.

Please acknowledge receipt of this Information Disclosure Statement and kindly make the cited references of record in the above-identified application.

Applicants believe this Information Disclosure Statement has been timely filed, however, the Commissioner is authorized to charge any fee due by way of this Information Disclosure Statement to our Deposit Account No. 19-1090.

Respectfully submitted,

Seed Intellectual Property Law Group PLLC

David V. Carlson

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Enclosures:

Form PTO-1449 Cited References (6)

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Sheet 1 of 1

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE ATTY. DOCKET NO. APPLICATION NO. (REV.7-80) PATENT AND TRADEMARK OFFICE 10/760,637 854063.737 APPLICANTS Chantal Auricchio et al. LOSURE STATEMENT ral sheets if necessary) GROUP ART UNIT FILING DATE 2818 January 20, 2004

## **U.S. PATENT DOCUMENTS**

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA	5,015,885	05/14/91	El Gamal et al.	307	465	
	AB	5,576,568	11/19/96	Kowshik	257	318	
	AC	5,587,603	12/24/96	Kowshik	257	316	
	AD	5,625,211	04/29/97	Kowshik	257	317	
	AE	RE 37,308 E	08/07/01	Cappelletti et al.	257	318	
	AF						
	AG						
	АН						
	Al						
	AJ						<u> </u>

### FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER DATE COUNTRY		TRANSLATION		
		DOCUMENT NUMBER	DATE	COUNTRI		NO
,	AK					
	AL			·		
	AM					
	AN					
	AO					

### OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AP	Borgatti, M. et al., "A Multi-Context 6.4Gb/s/Channel On-Chip Communication Network Using 0.18µm Flash-EEPROM Switches and Elastic Interconnects," <i>STMicroelectronics</i> , February 12, 2003.
AQ	
AR	

**EXAMINER** 

DATE CONSIDERED

\* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).